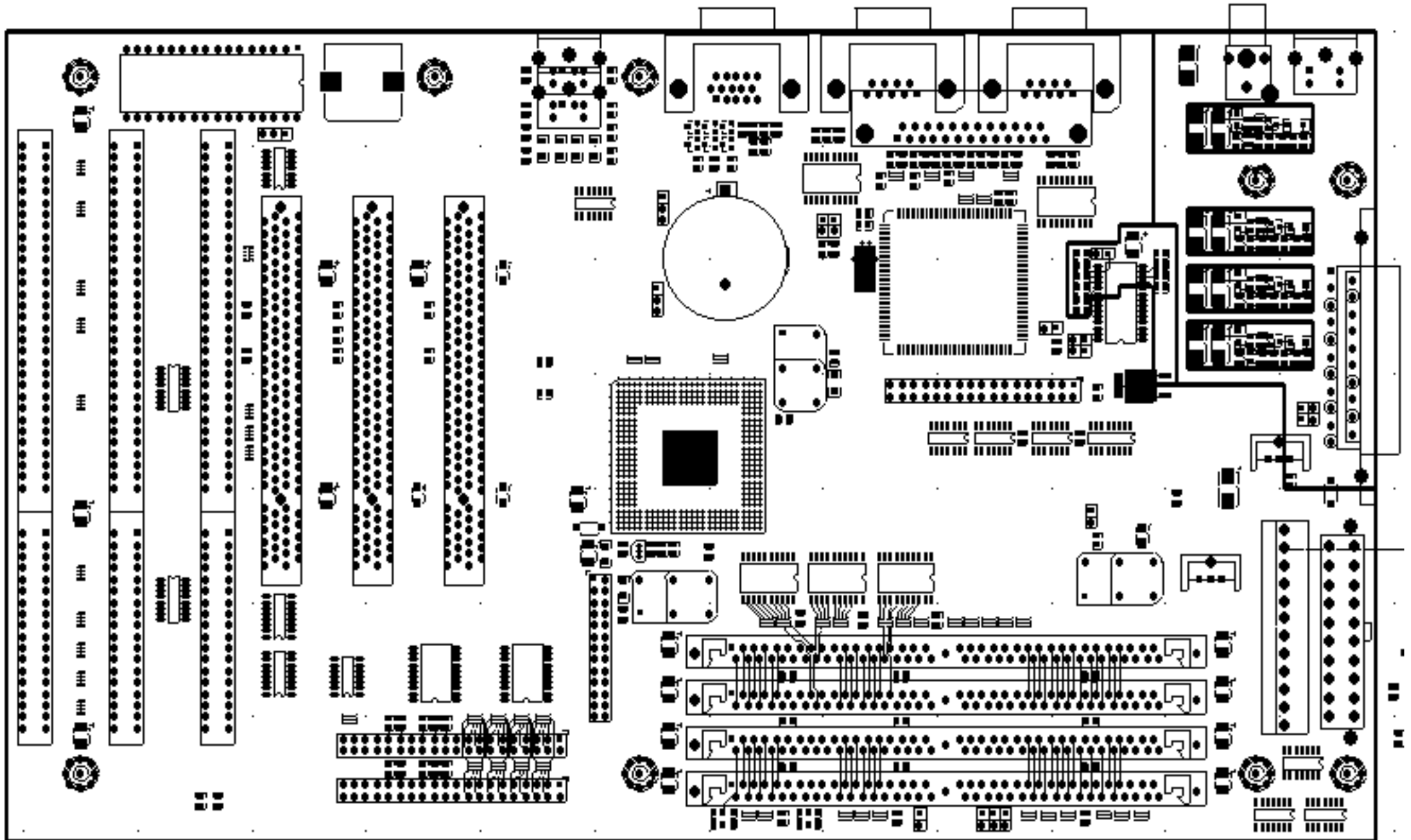
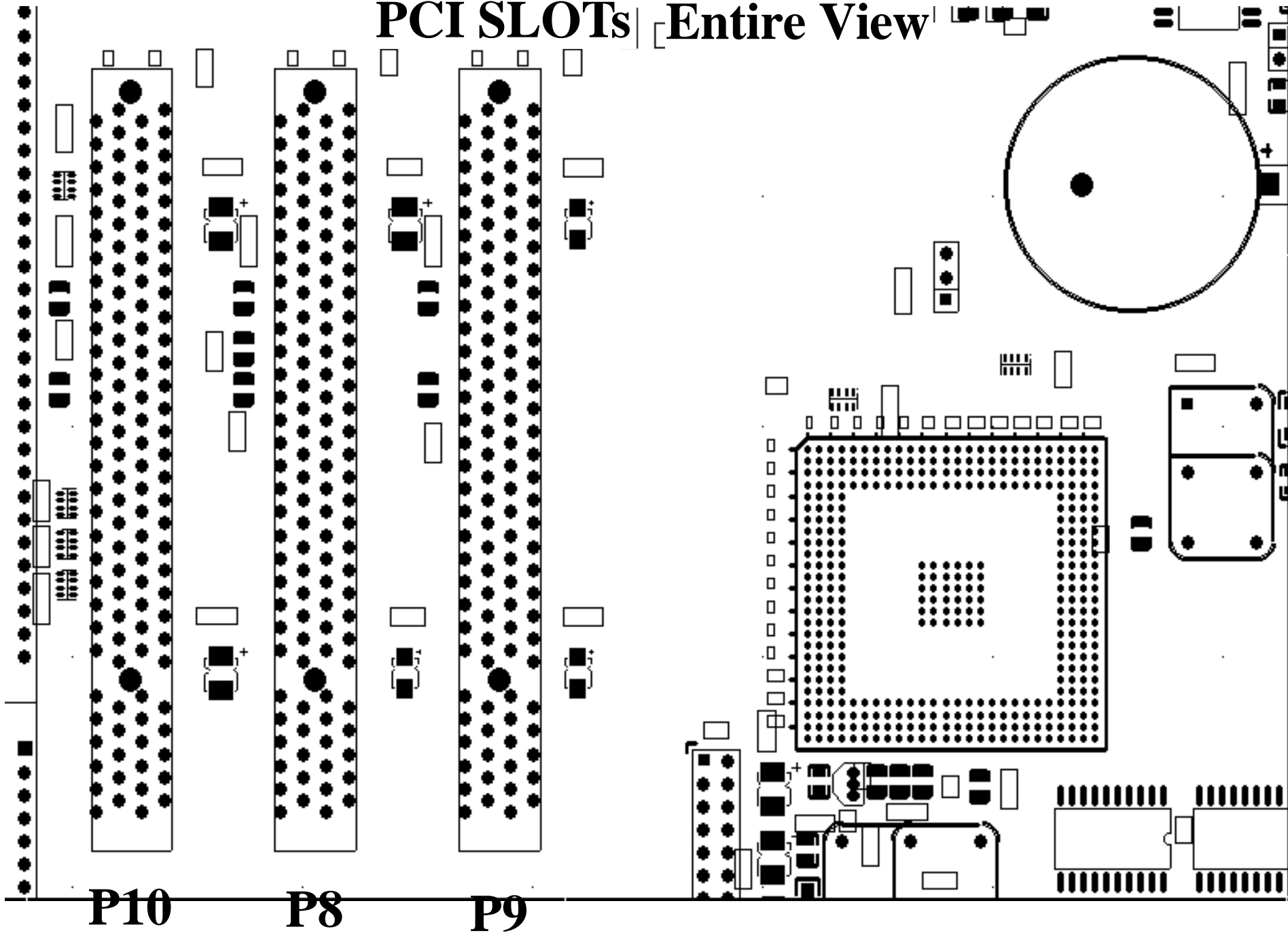


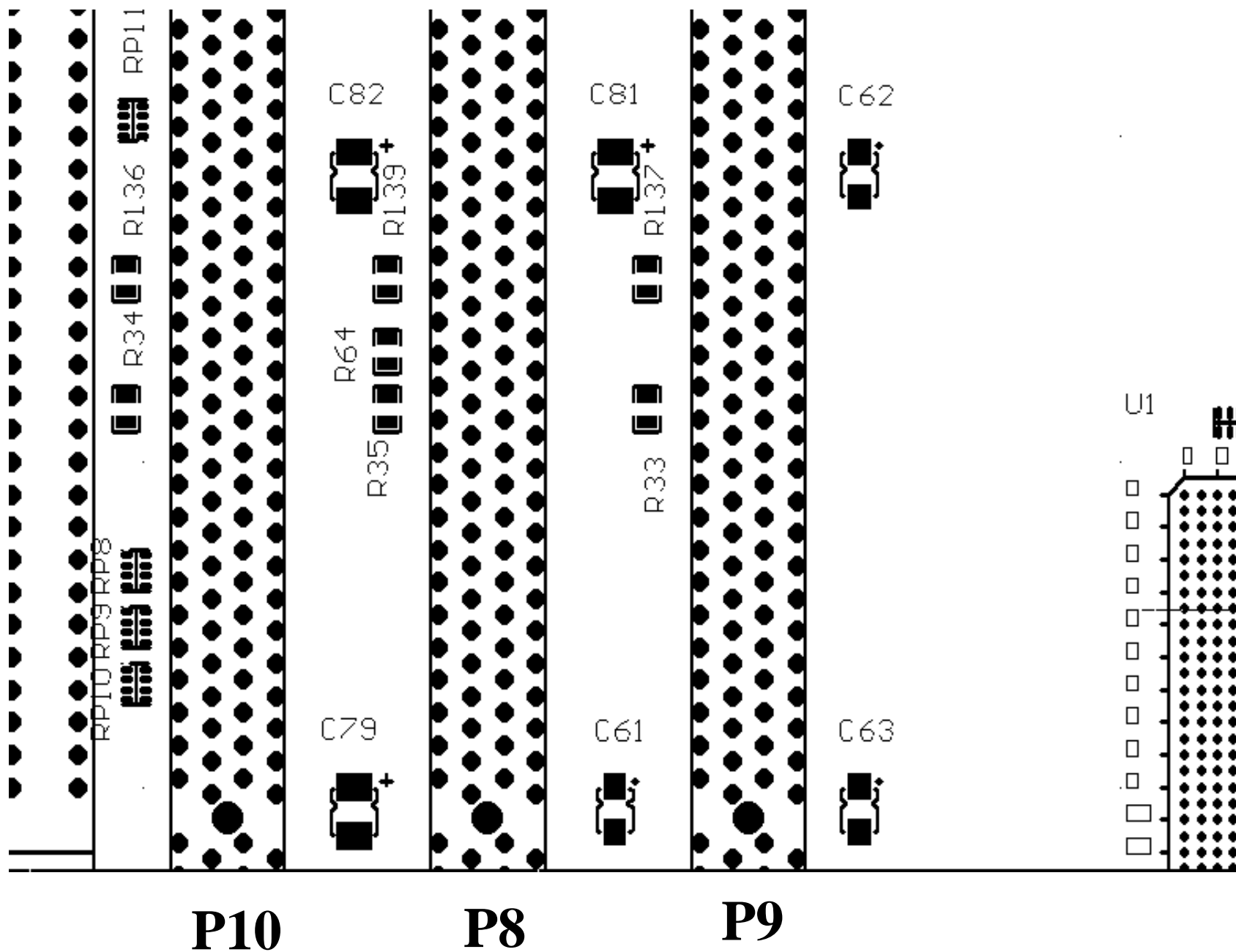
# Board Overview



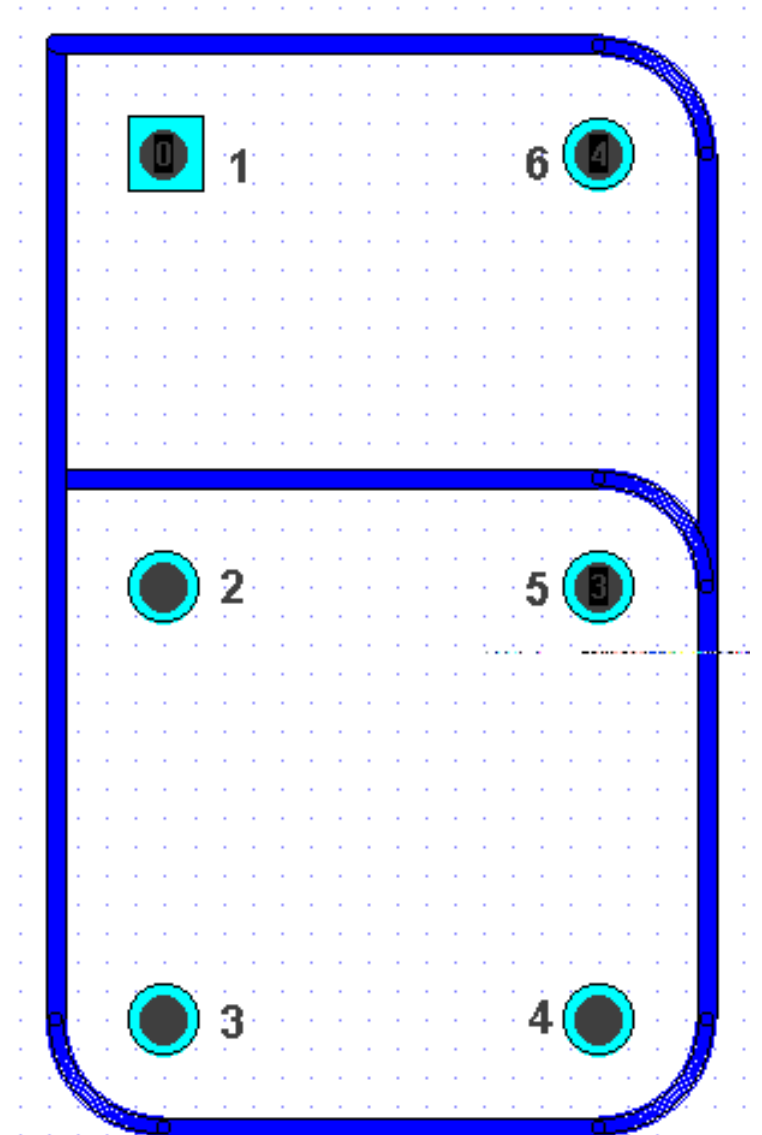
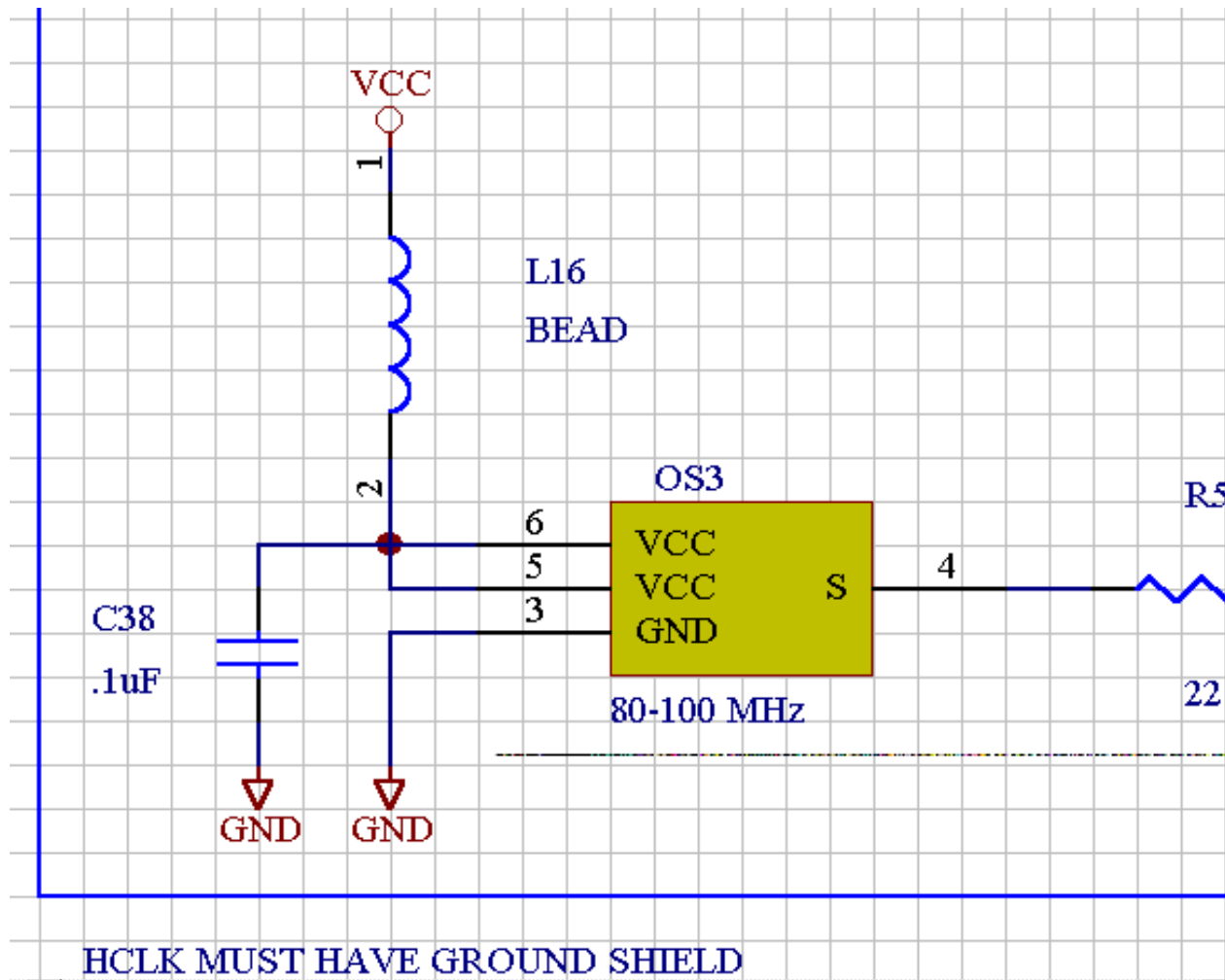
# PCI SLOTS | Entire View



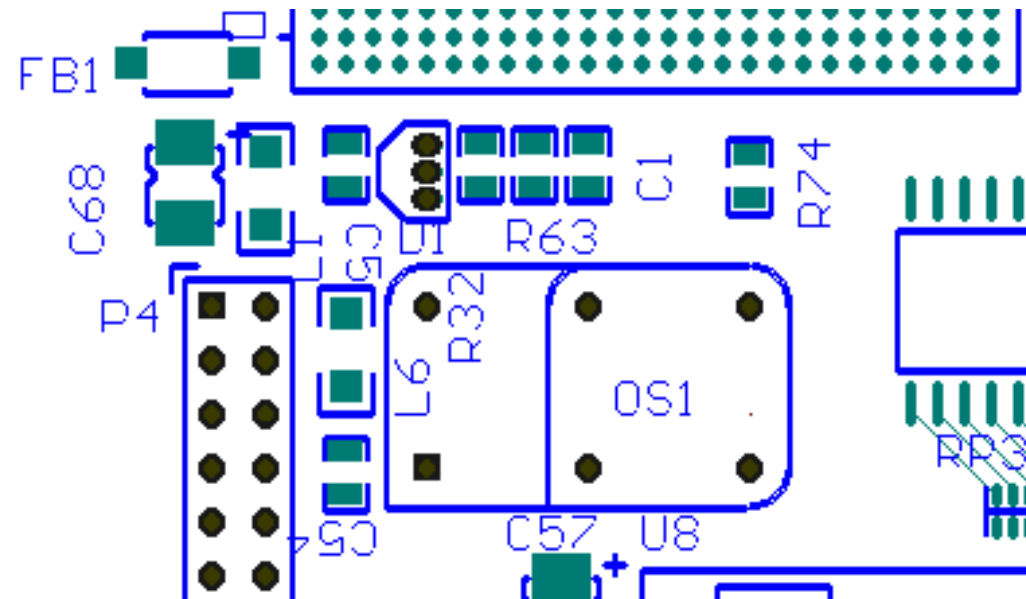
# PCI SLOTS Enlarged View



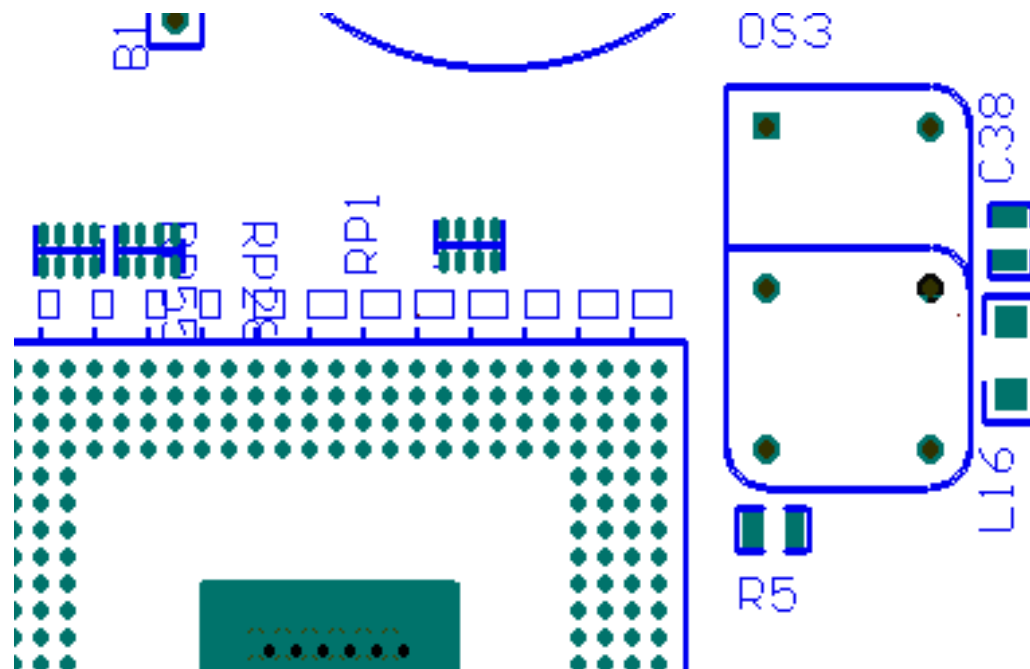
# OSCILLATOR



## Oscillator 1 (14 MHz)

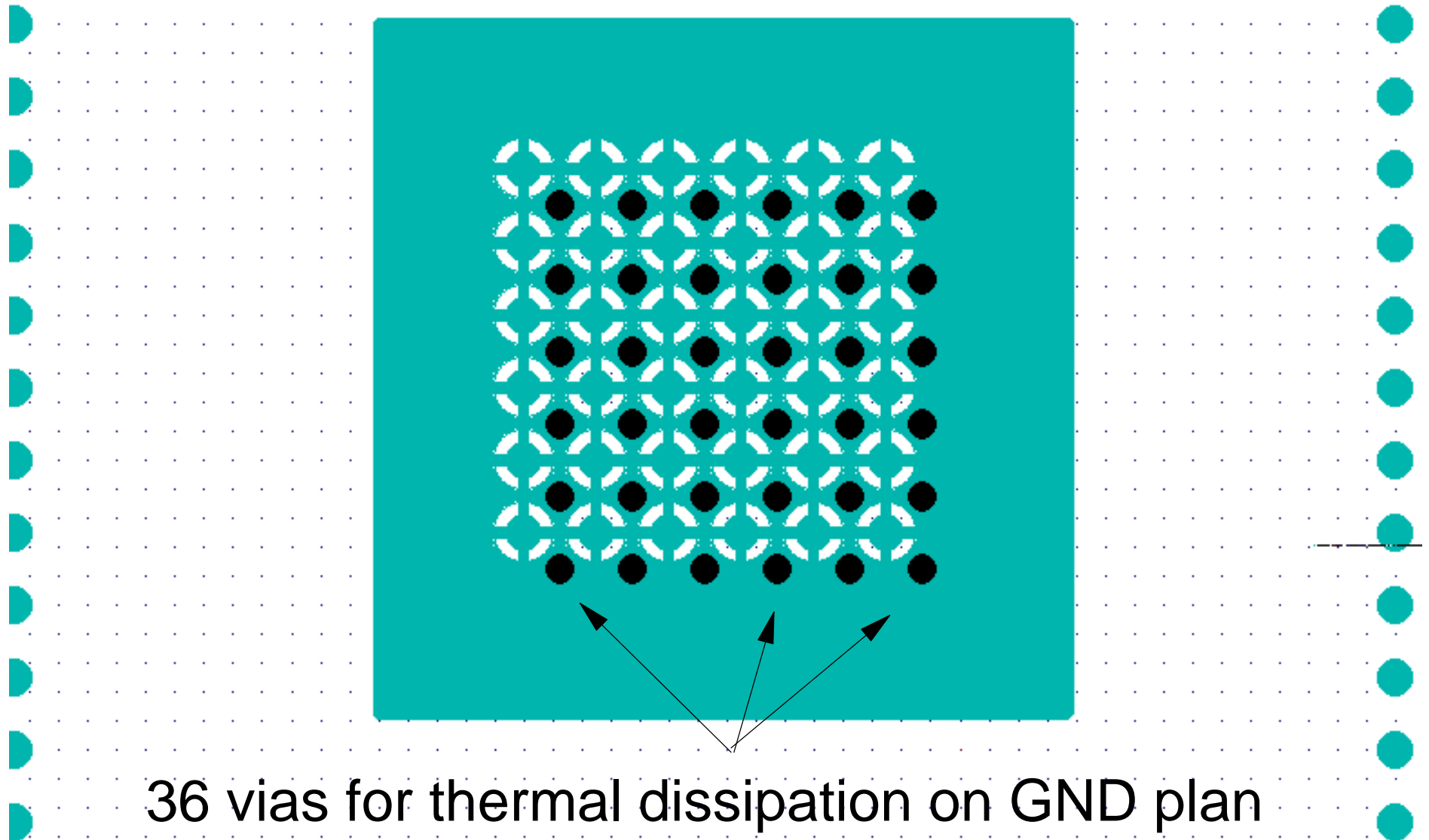


## Oscillator 3 (80-100 MHz)

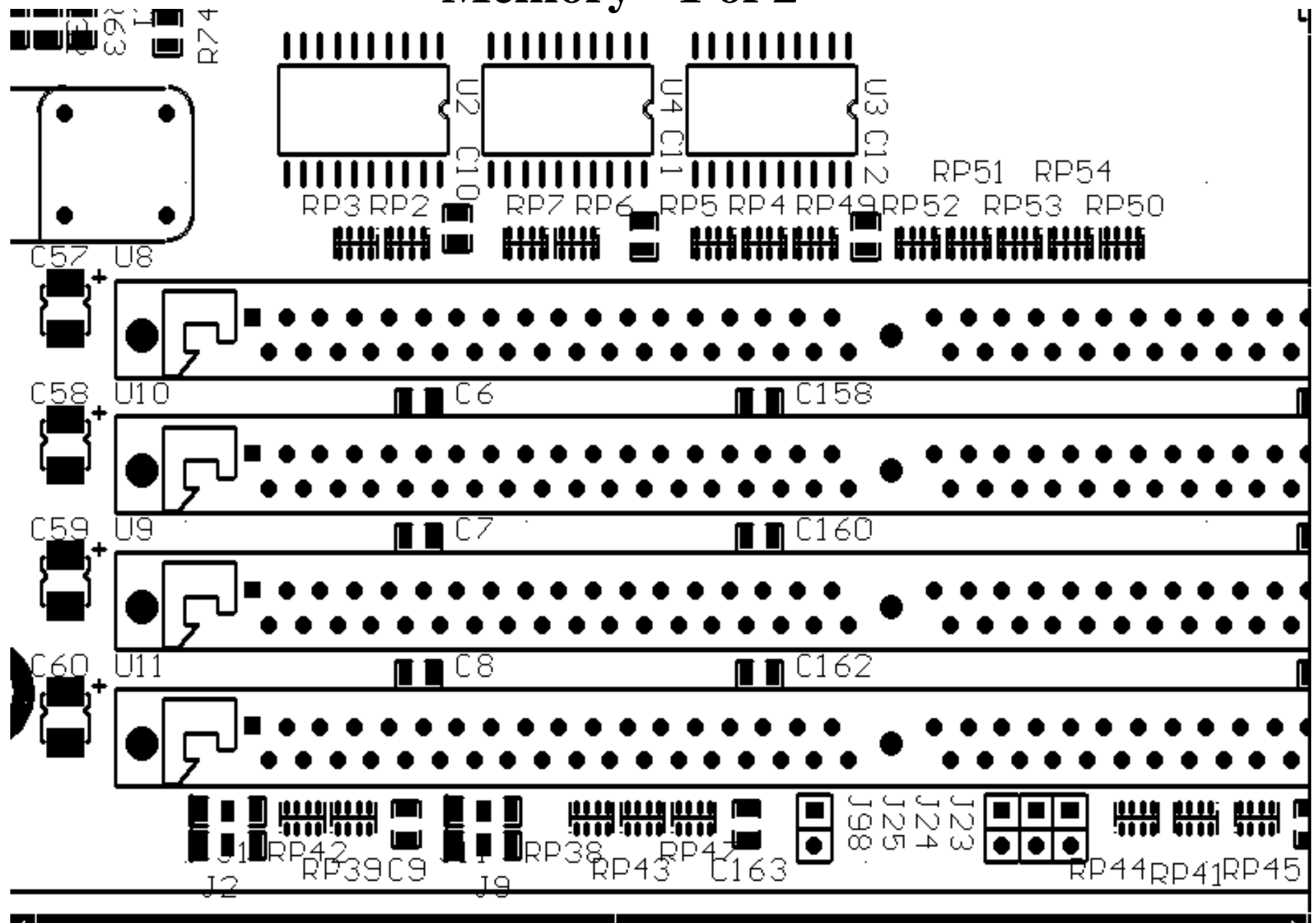


# BGA - Thermal Dissipation

Local plan on component side AND solder side:

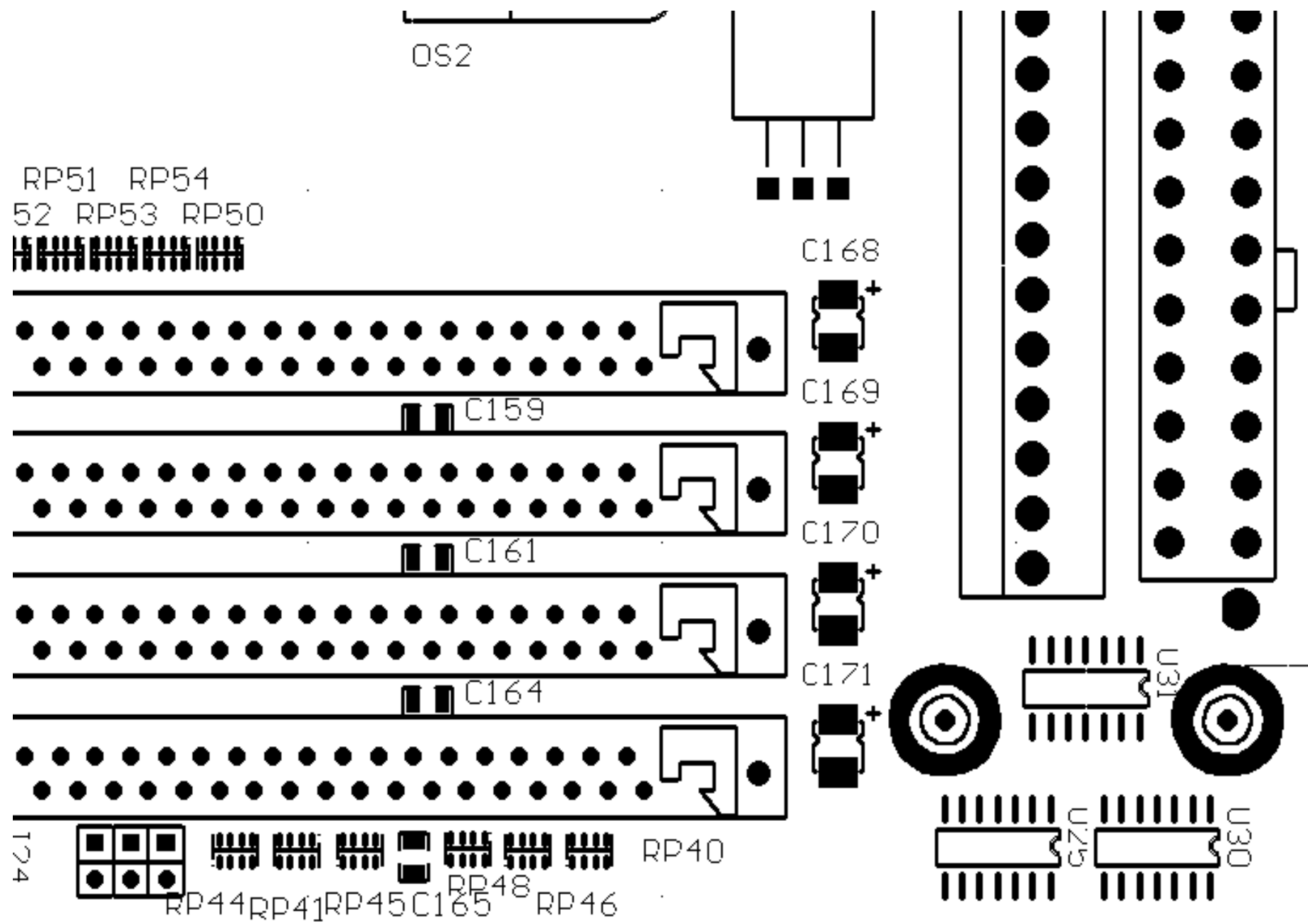


## Memory - 1 of 2

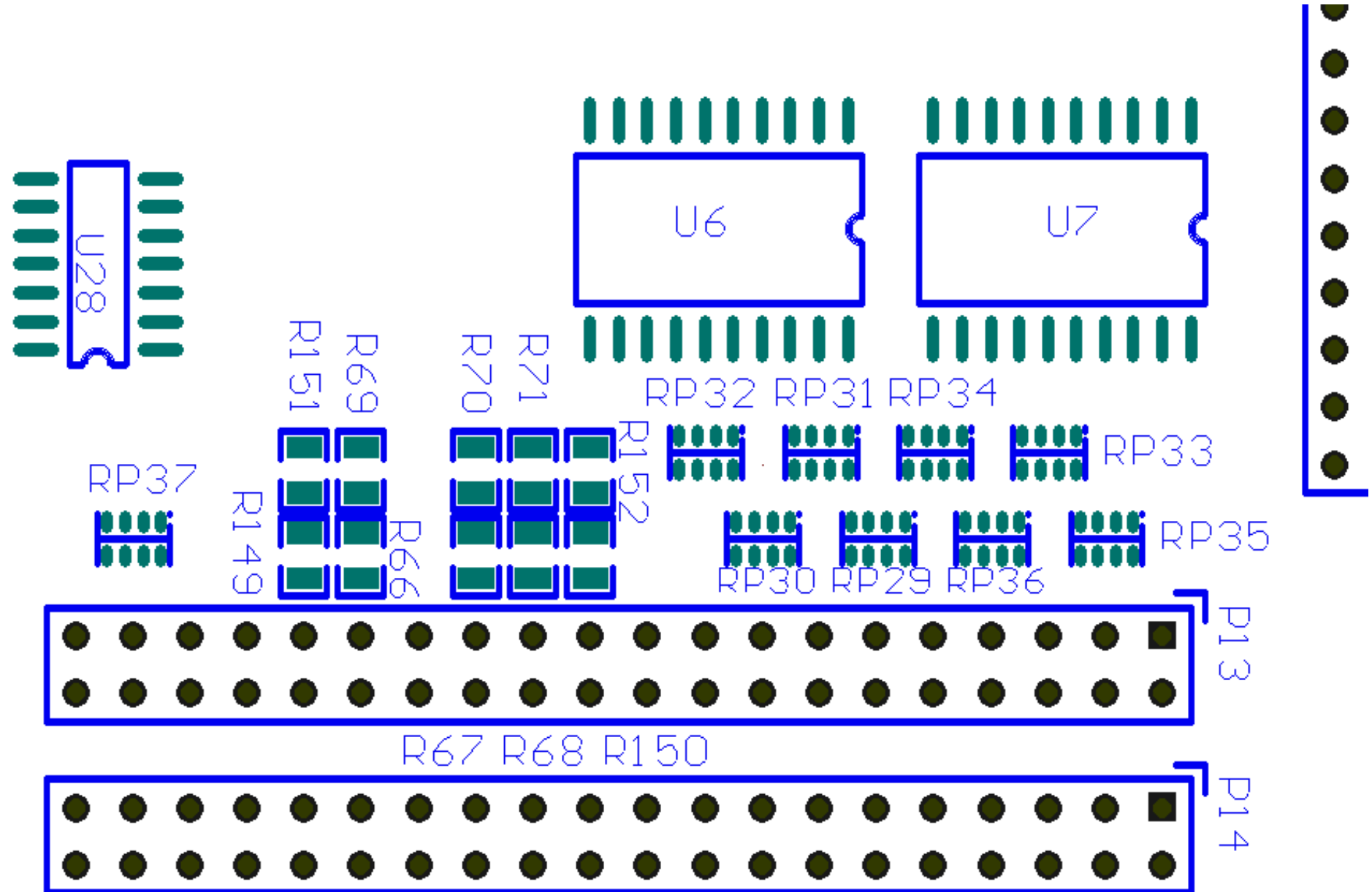




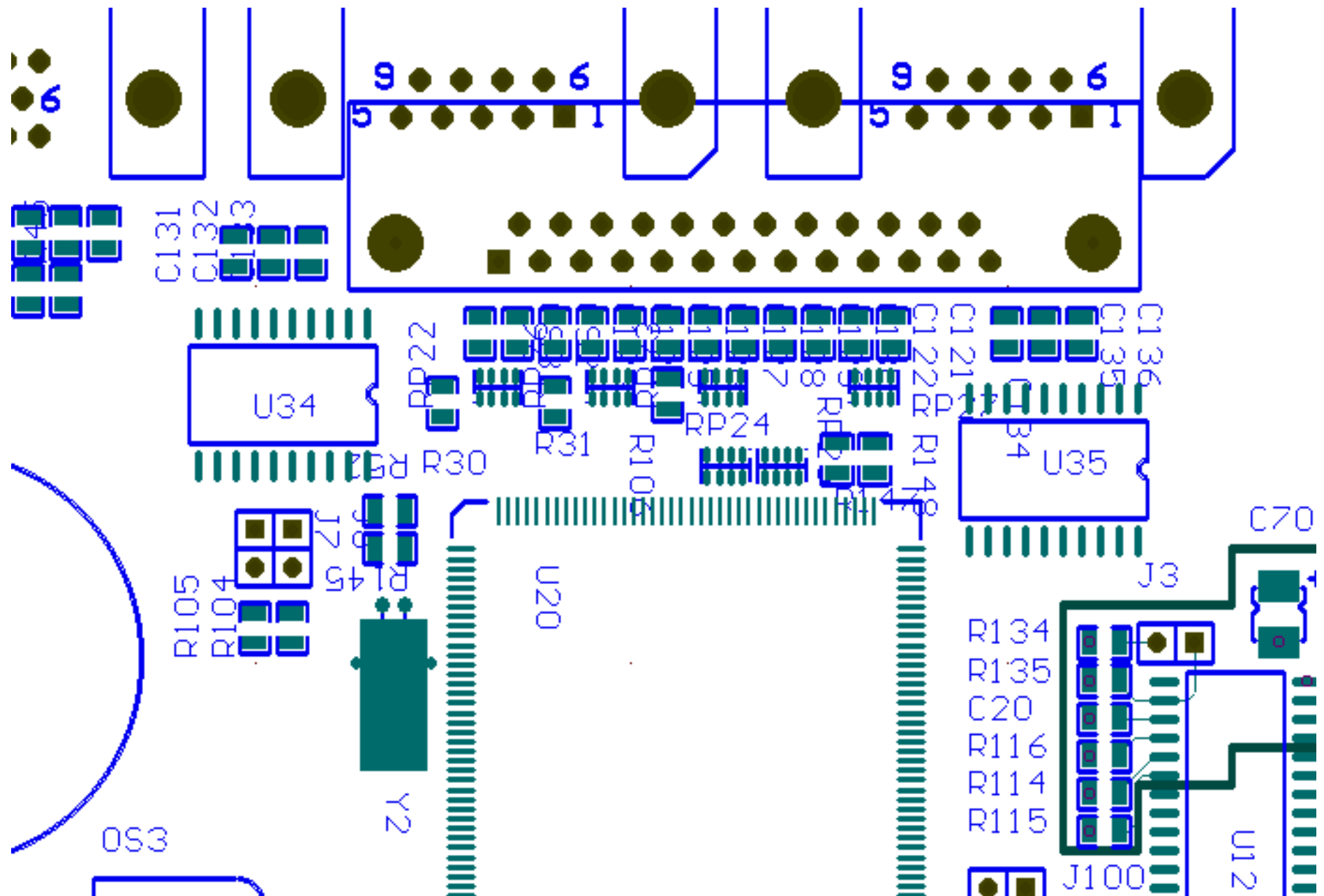
# Memory - 2 of 2



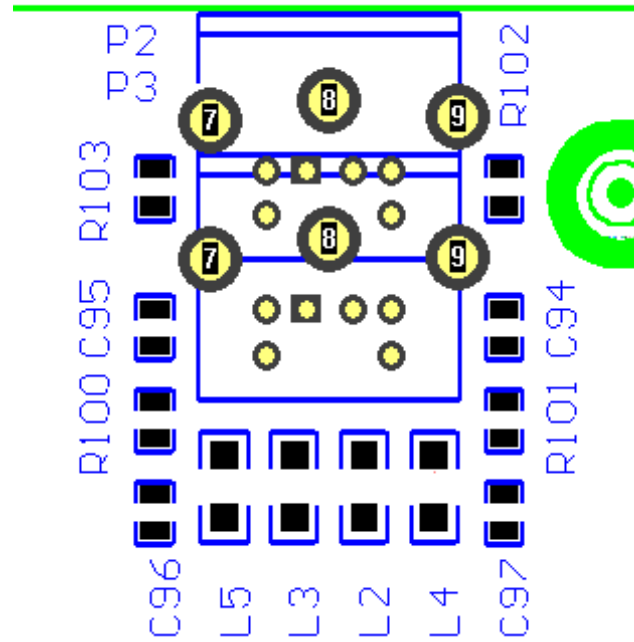
# IDE



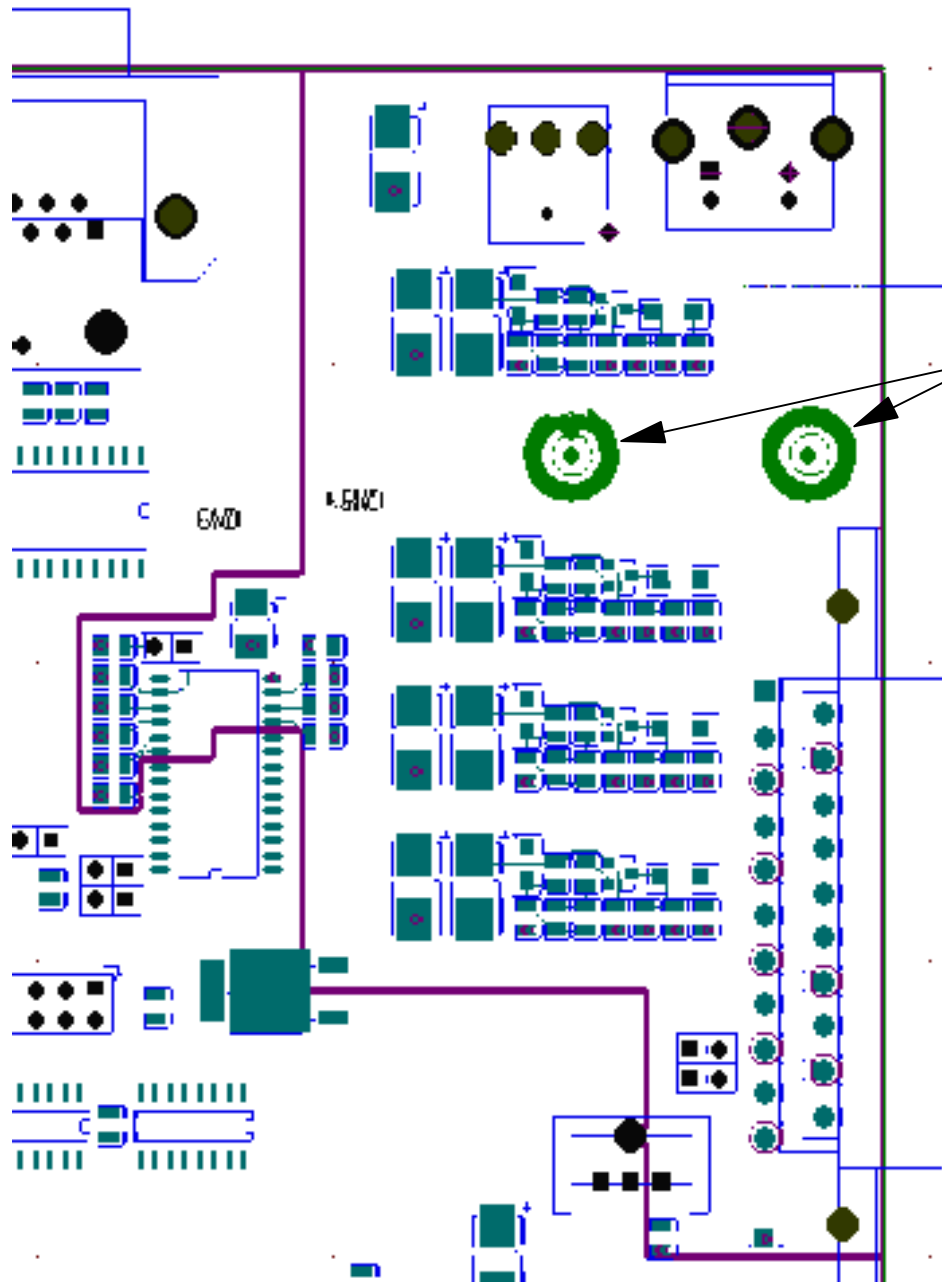
# Super I/O



# Keyboard and Mouse In A Single Connector

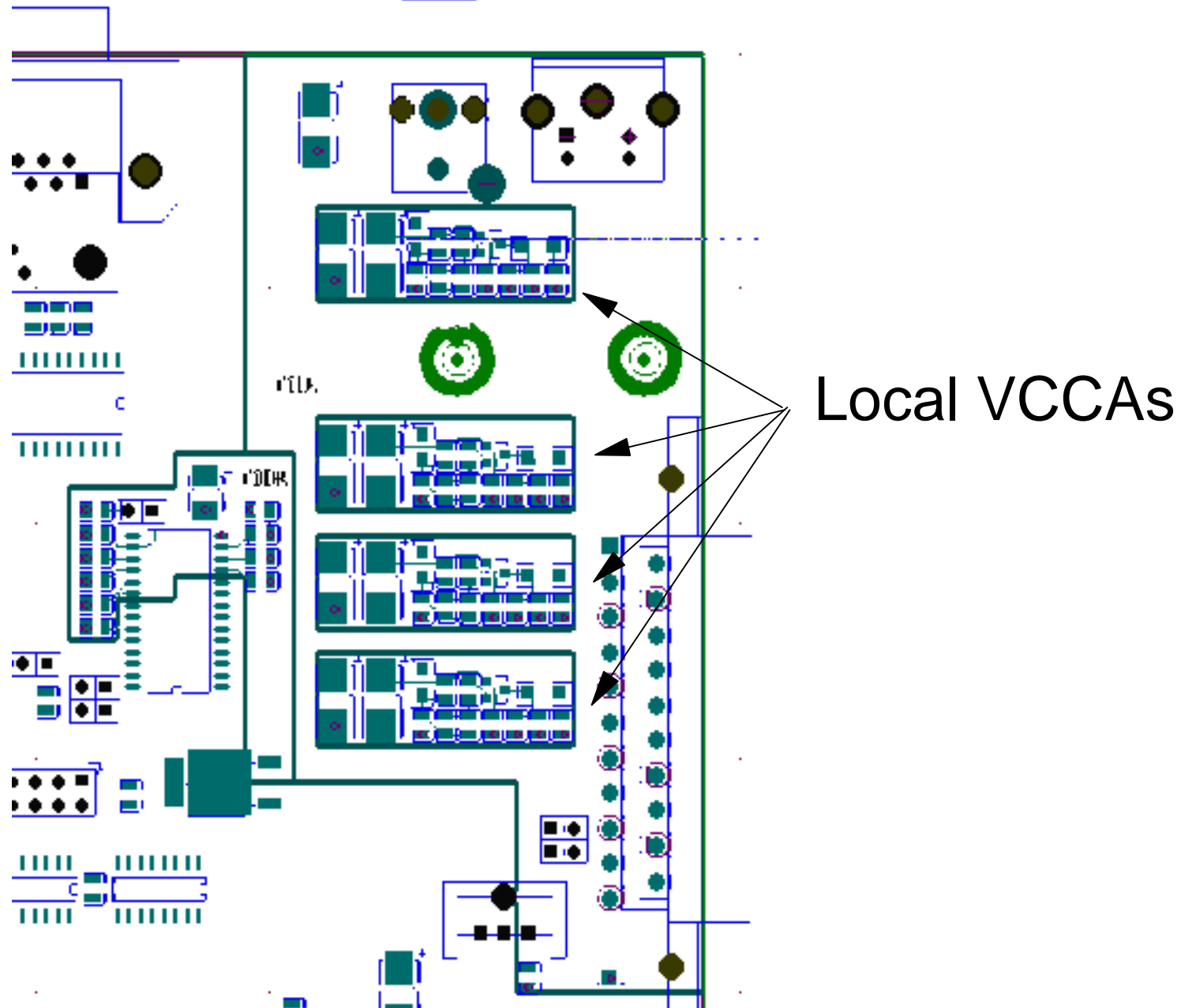


# Analog Plan - AGND



Do not connect to AGND

# Analog Plan - Power



# **TV Output from STV0119**

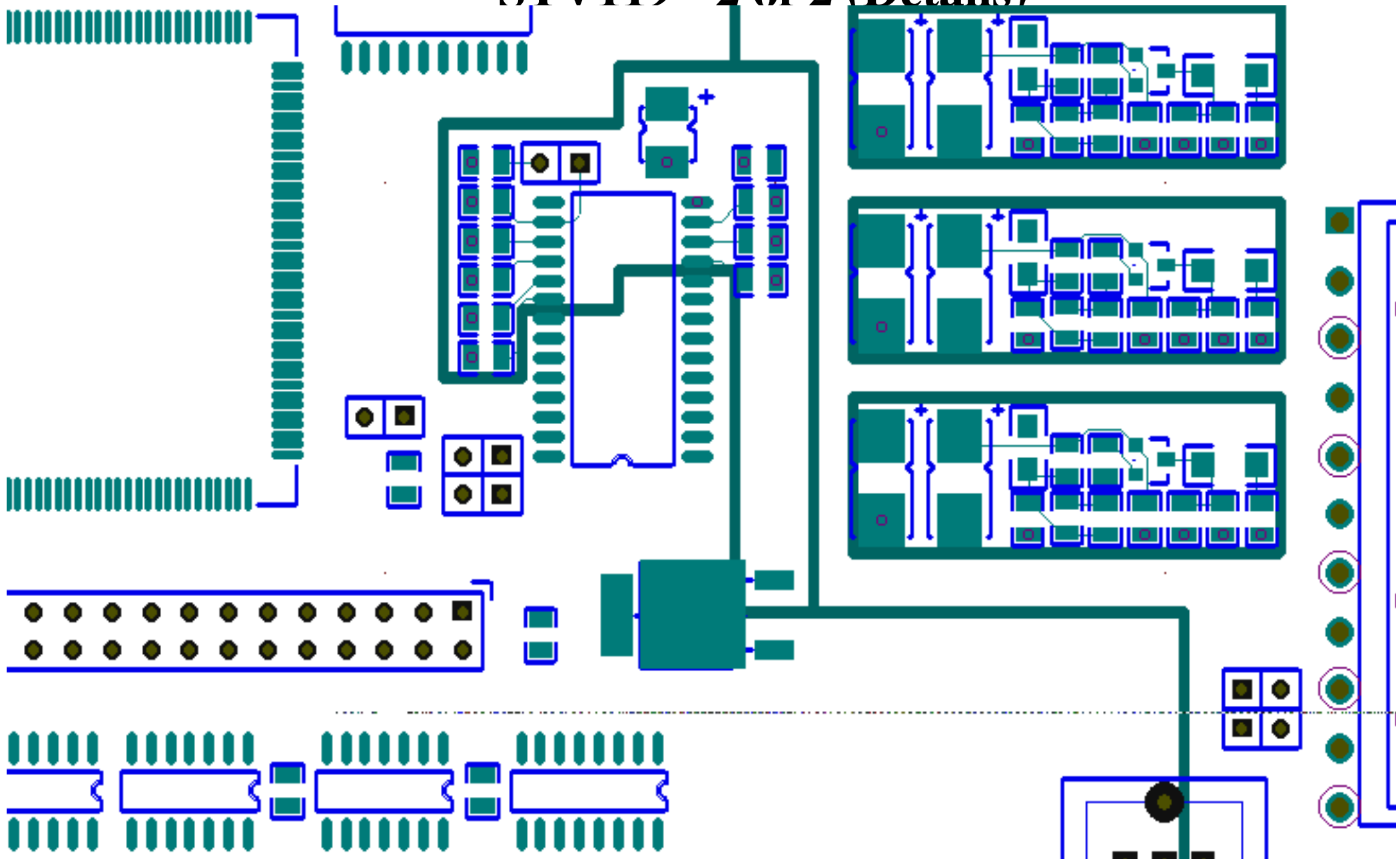
**This TV Output placement is the same for all the signals output from STV0119.**

- The components as described in next pages should remain as they are.**
- R113, R114, R115, R116 to be placed near to STV0119**
- L12.1, L13.1, L14.1, L15.1 to be connected to VCCA**
- Analog Ground of all the components in this section to be connected as a STAR**
- 100uF (D footprint) can be replaced with non-smd capacitors**

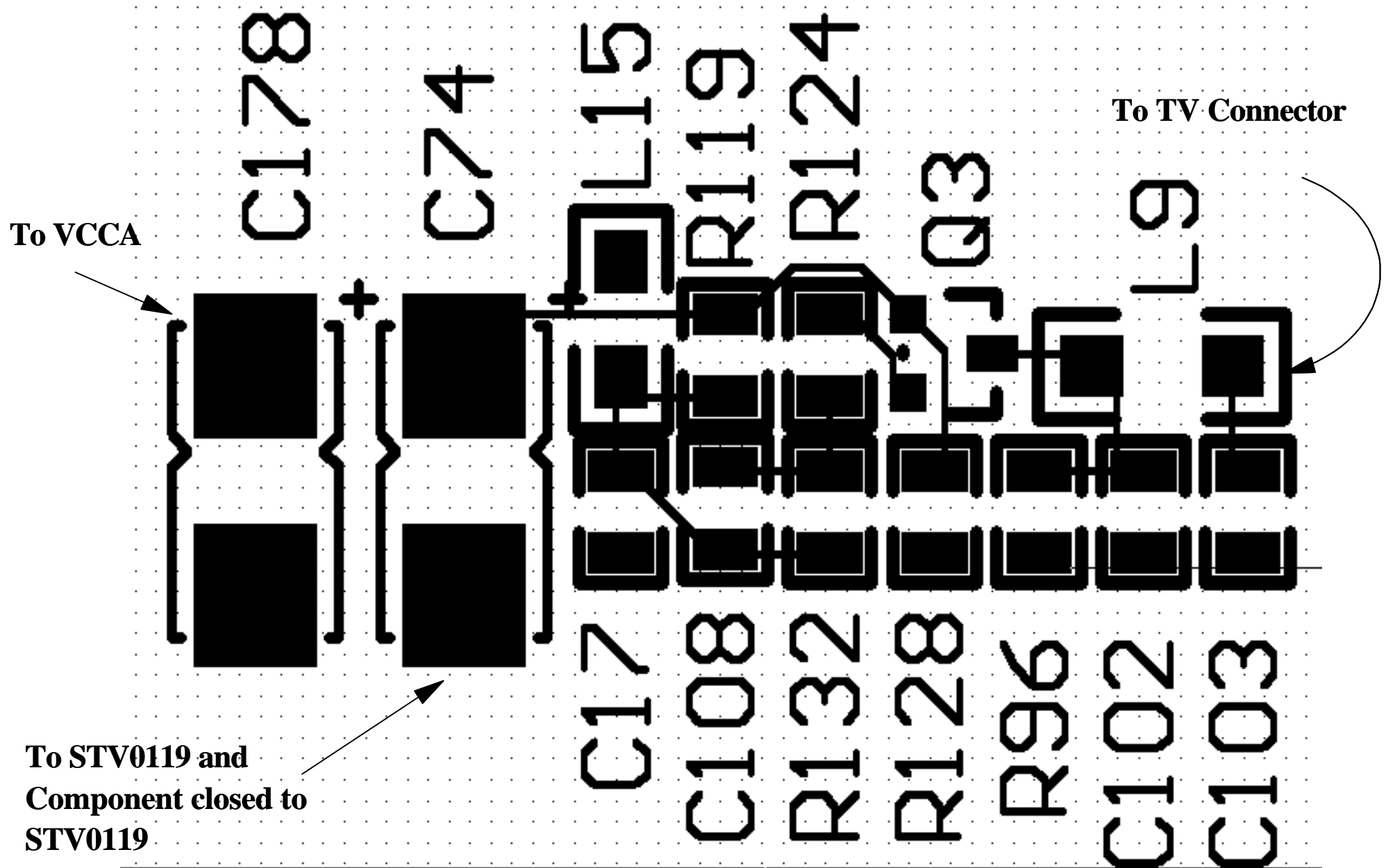




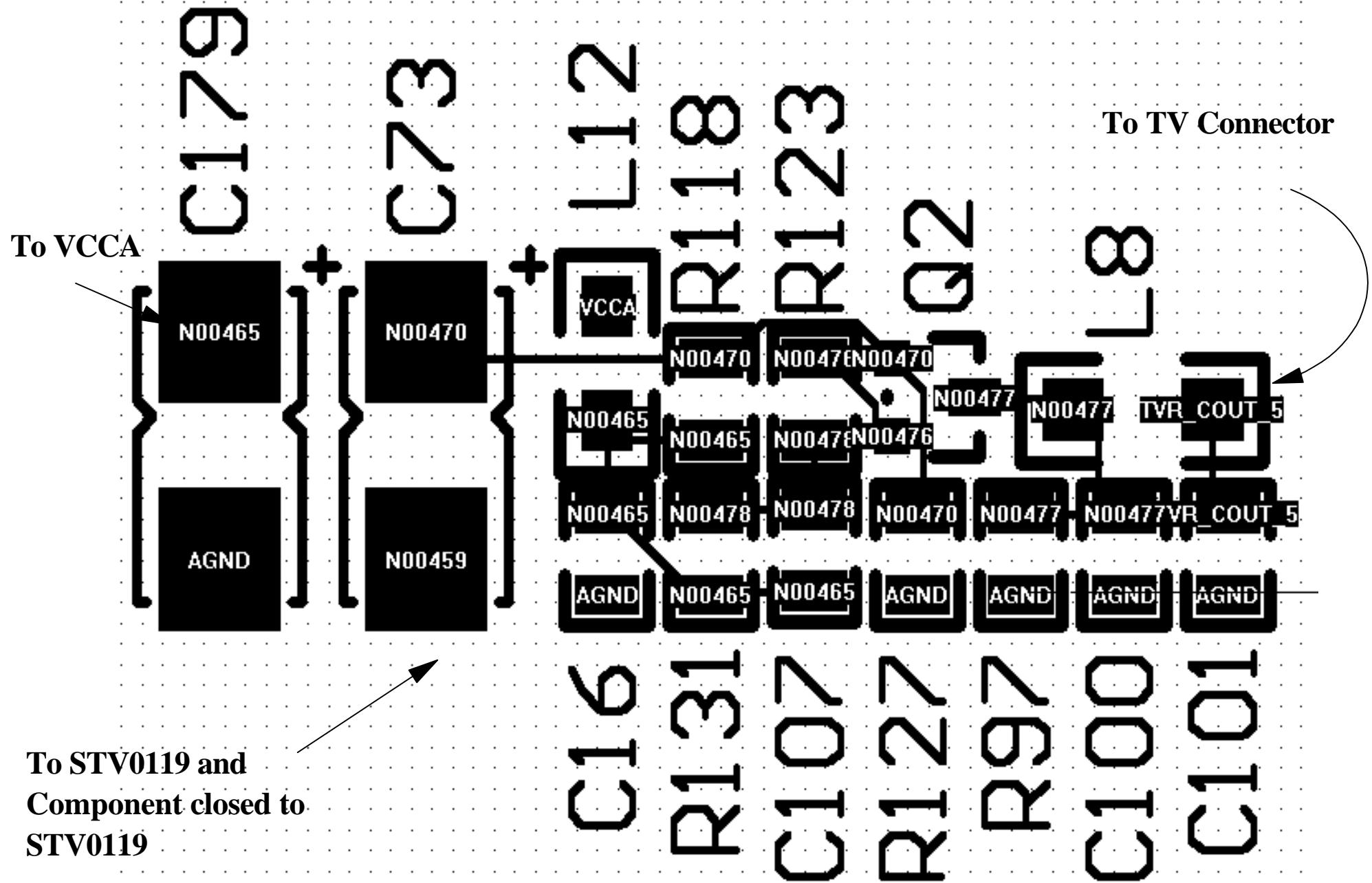
## STV119 - 2 of 2 (Details)



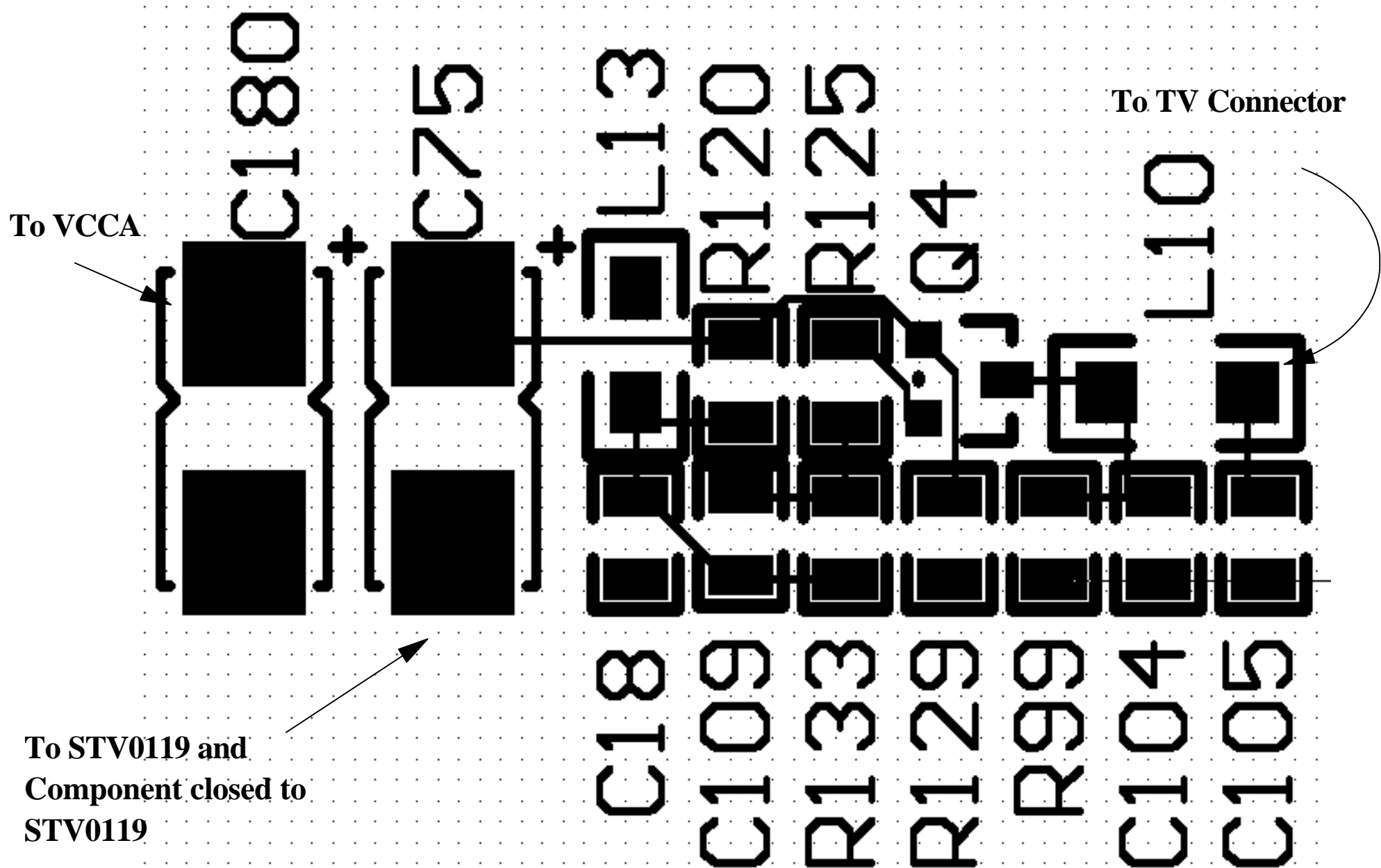
# TV Output 1 of 4 this block should remain as placed



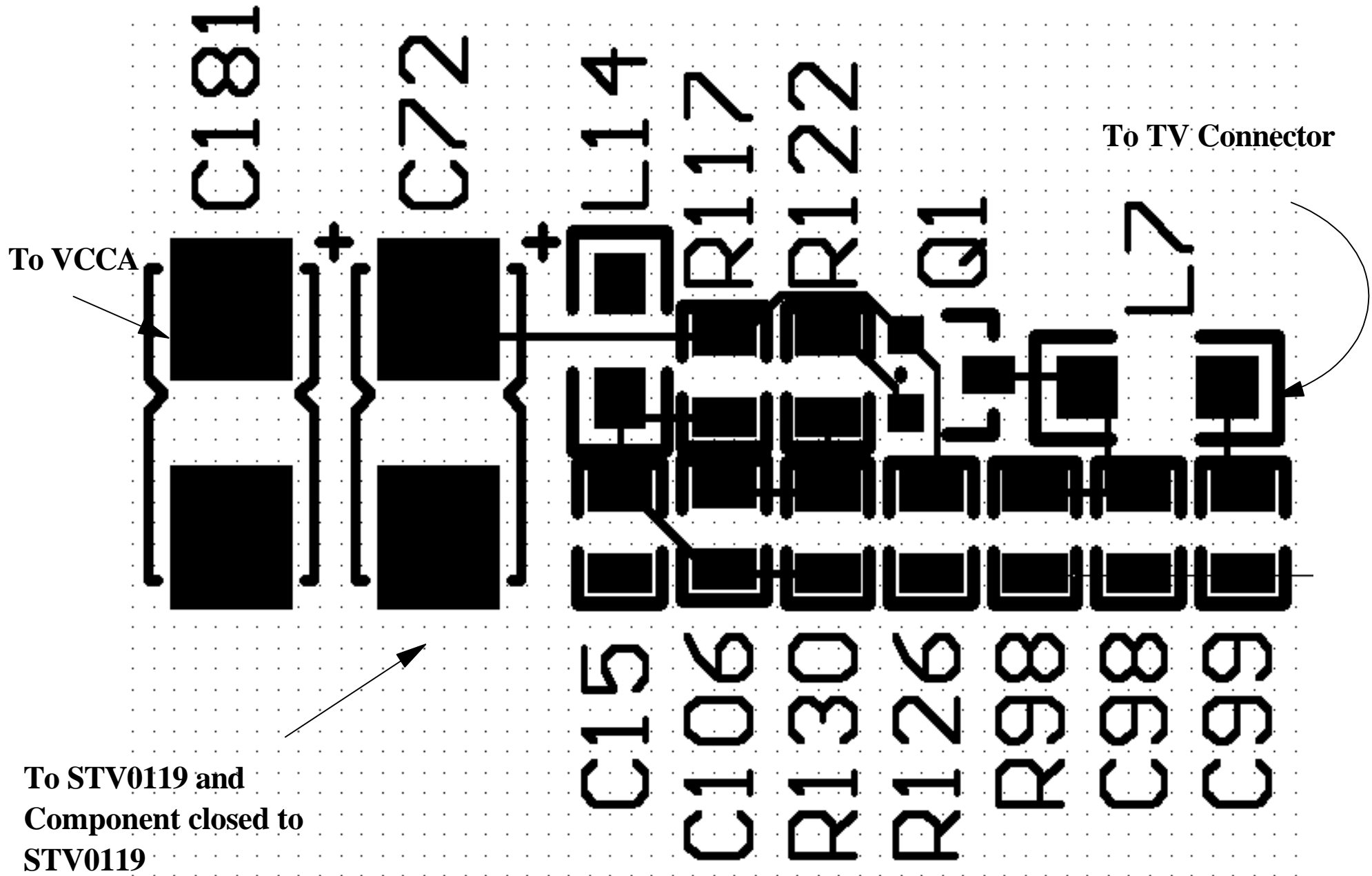
## TV Output 2 of 4 this block should remain as placed



# TV Output 3 of 4 this block should remain as placed



# TV Output 4 of 4 this block should remain as placed



## TV Output - Routing guidelines

